



# STV9306

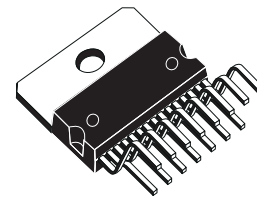
## BUS CONTROLLED VERTICAL DEFLECTION SYSTEM WITH EAST/WEST CORRECTION OUTPUT CIRCUIT

PRELIMINARY DATA

- FULLY I<sup>2</sup>C CONTROLLED
- DMOS POWER HALF-BRIDGE AMPLIFIER
- DC COUPLED OPERATION
- INTERNAL FLYBACK GENERATOR (UP TO 60V)
- SELF ADAPTED SAWTOOTH (50/60Hz)
- 100Hz OPERATION
- VERTICAL LINEARITY, AMPLITUDE AND CENTERING ADJUSTMENTS
- HORIZONTAL WIDTH, PINCUSHION, TRAPEZOID AND CORNER ADJUSTMENTS
- BREATHING CORRECTION
- 4/3, 16/9 CRT APPLICATION
- THERMAL PROTECTION
- LINEAR VERTICAL ZOOM FUNCTION
- E/W CLASS A OUTPUT
- LOW EXTERNAL COMPONENTS

### DESCRIPTION

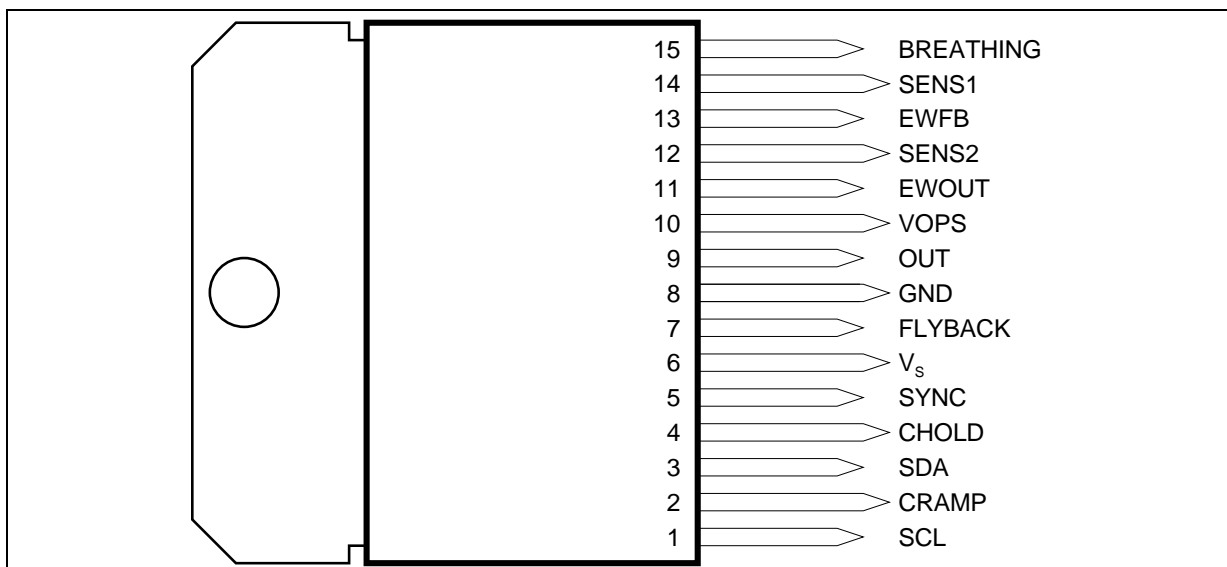
The STV9306 is a fully I<sup>2</sup>C controlled vertical deflection IC designed for use in 110°, 4/3 or 16/9 CRT applications. It integrates both the vertical deflection and E/W correction circuitries necessary in design of a 110° chassis.



**MULTIWATT15**  
(Plastic Package)

**ORDER CODE : STV9306**

### PIN CONNECTIONS



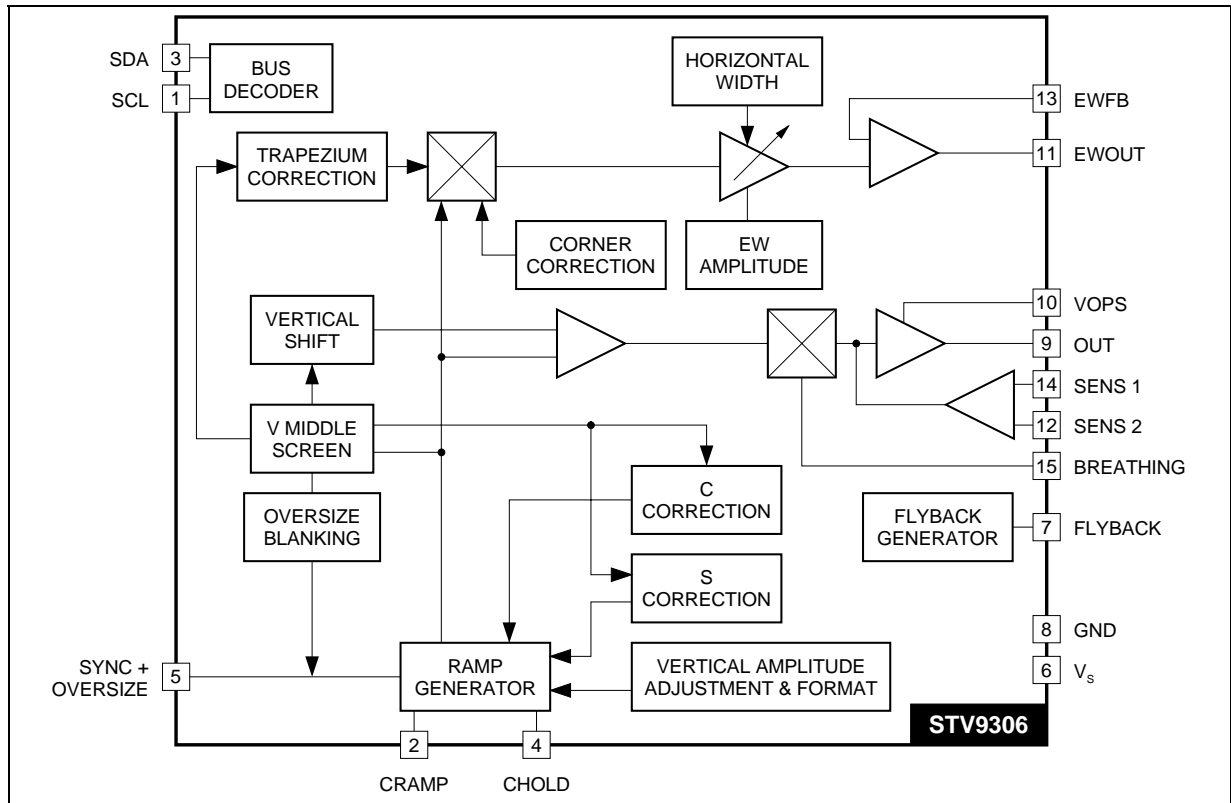
9306-01 LPS

PIN LIST

Pin	Symbol	Description
1	SCL	I <sup>2</sup> C Bus Clock
2	CRAMP	Ramp Capacitor
3	SDA	I <sup>2</sup> C Bus Data
4	CHOLD	Hold Capacitor
5	SYNC	Sync Input
6	VS	Supply Voltage
7	FLYBACK	Flyback Output
8	GND	Ground
9	OUT	Vertical Output
10	VOPS	Vertical Output Power Supply
11	EWOUT	EW Output
12	SENS2	Vertical Current Sense 2
13	EWFB	EW Feedback
14	SENS1	Vertical Current Sense 1
15	BREATHING	Breathing Input

9306-01.TBL

BLOCK DIAGRAM



9306-02.EPS