

X541UV Repair Guide

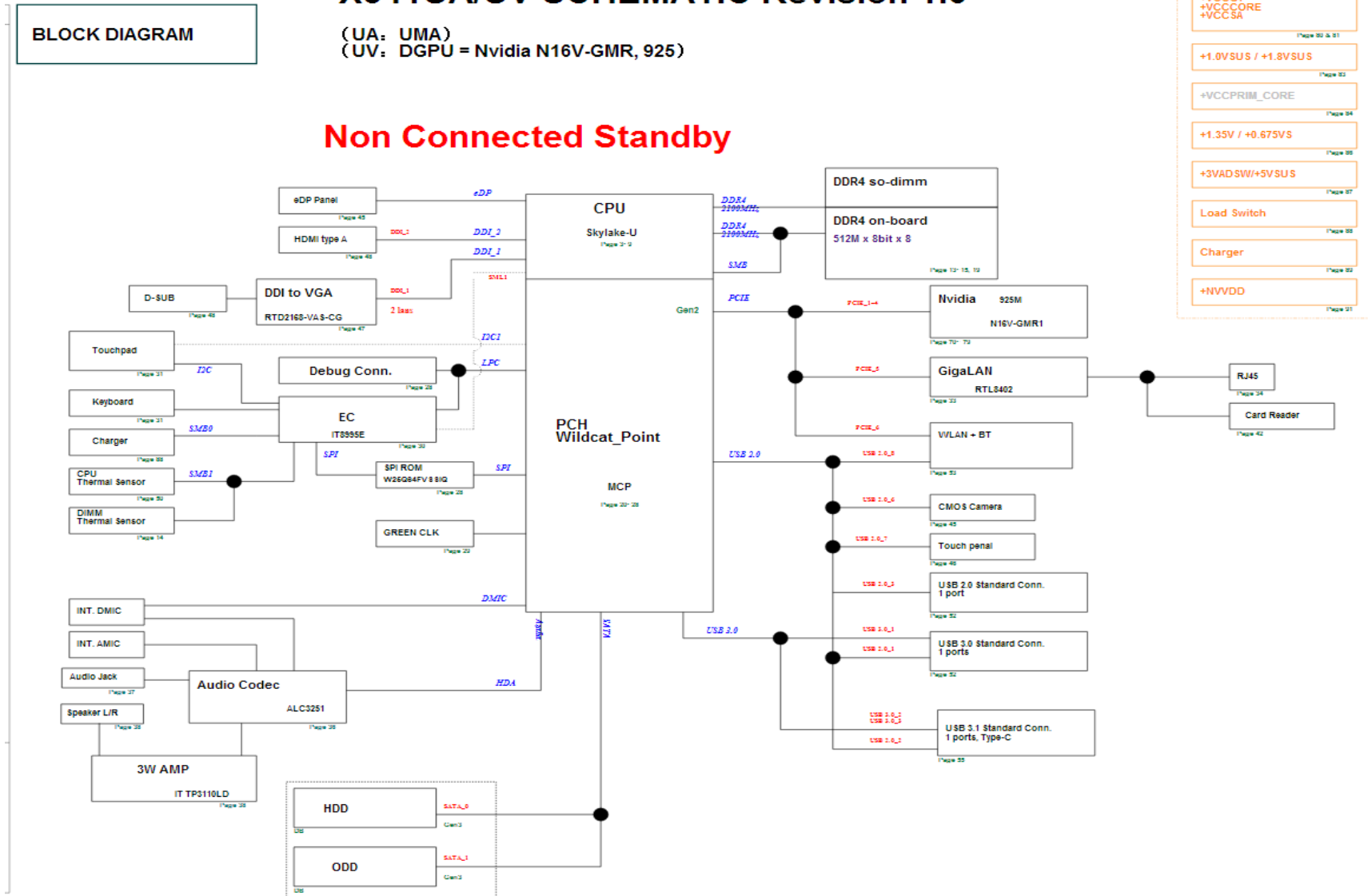
BLOCK DIAGRAM

X541UA/UV SCHEMATIC Revision 1.0

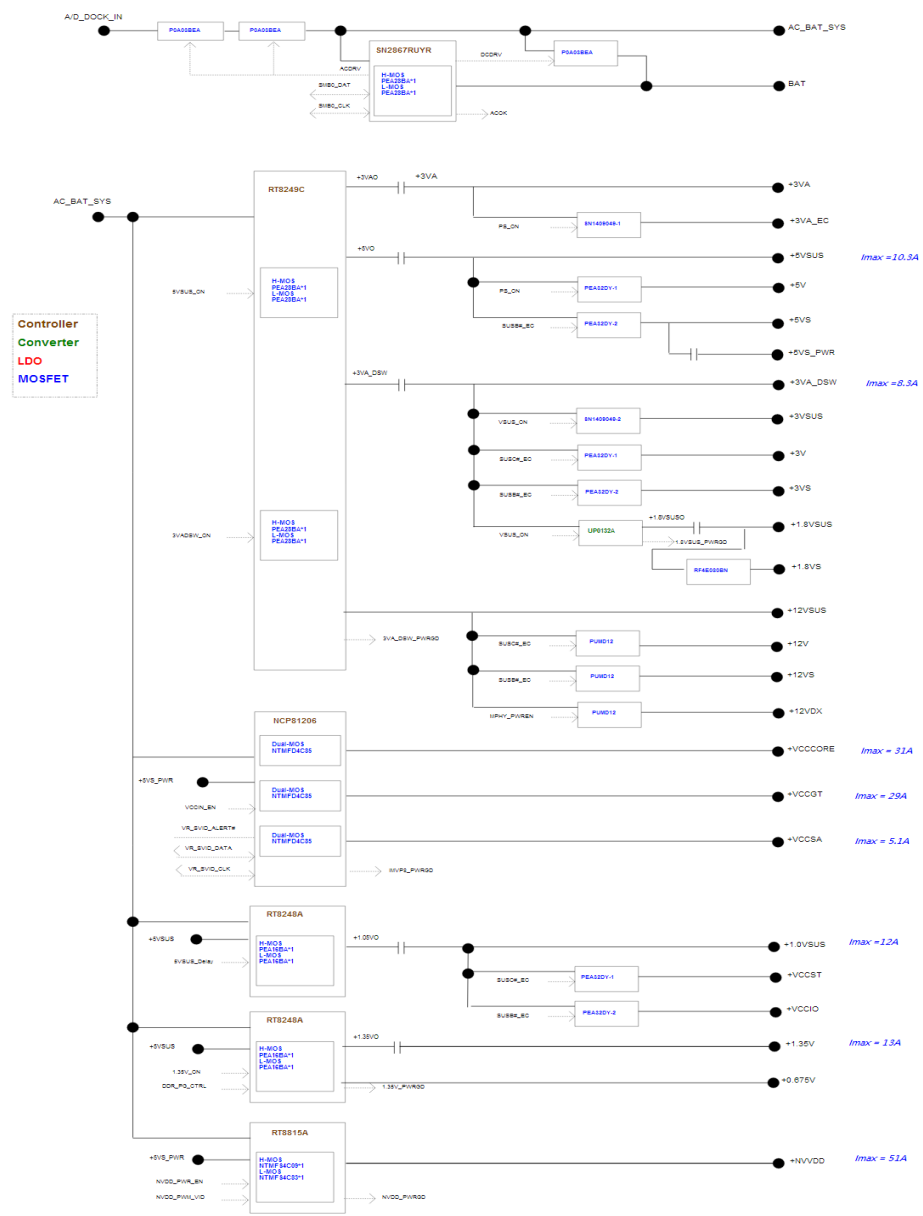
(UA: UMA)
(UV: DGPU = Nvidia N16V-GMR, 925)

BLOCK DIAGRAM

Non Connected Standby

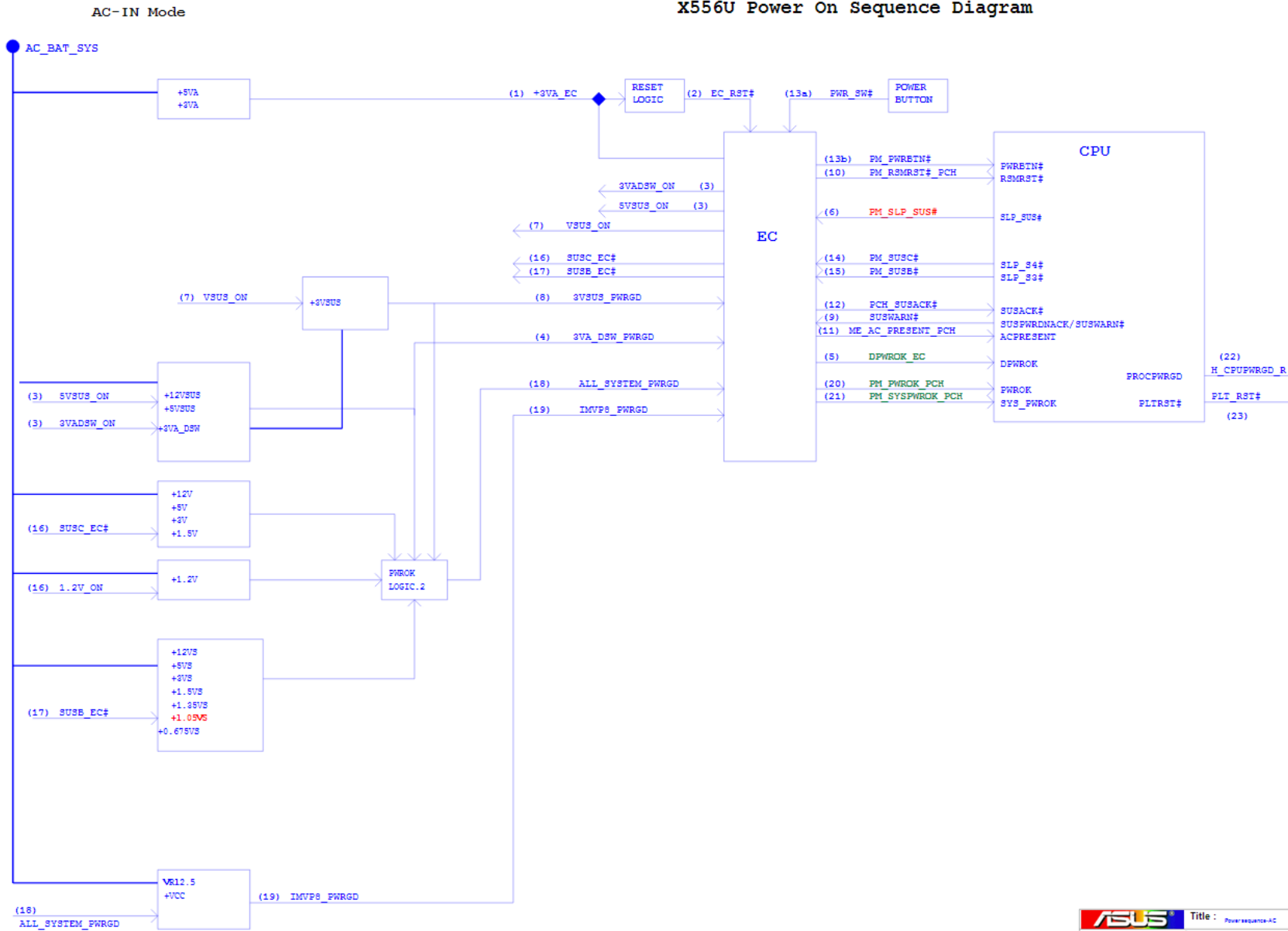


POWER FLOW

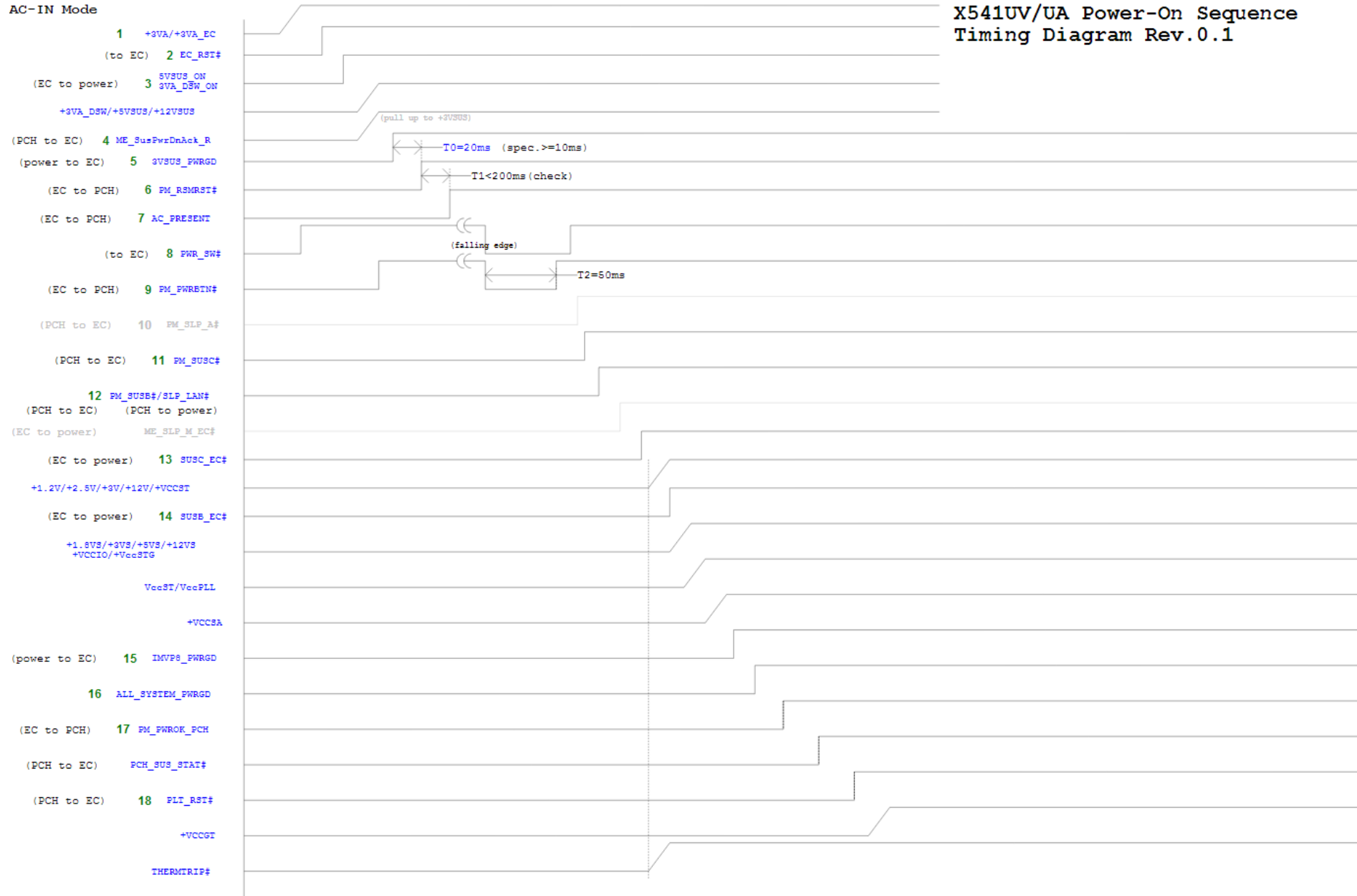


POWER ON SEQUENCE

X556U Power On Sequence Diagram



AC Mode Power On Timing

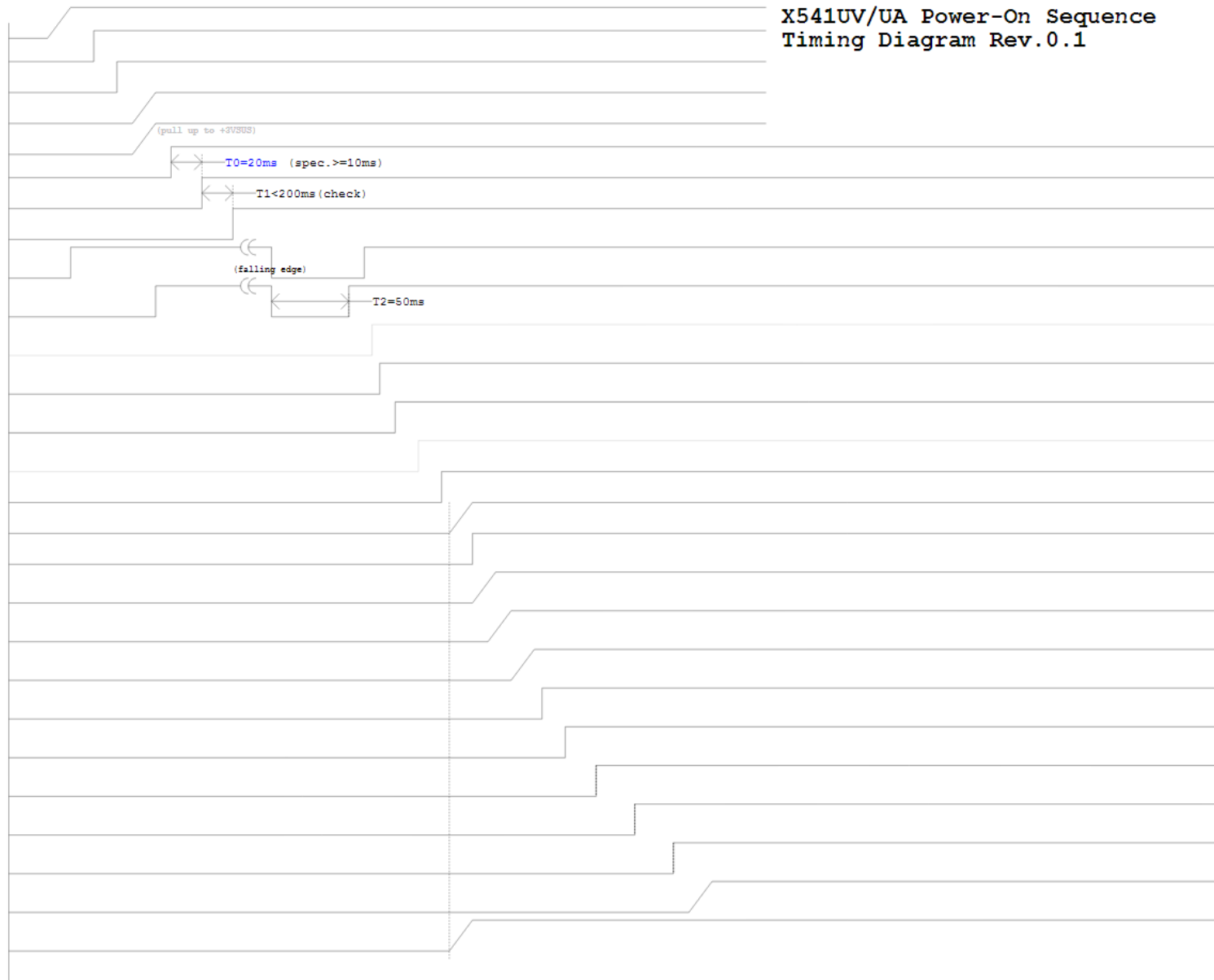


DC Mode Power On Timing

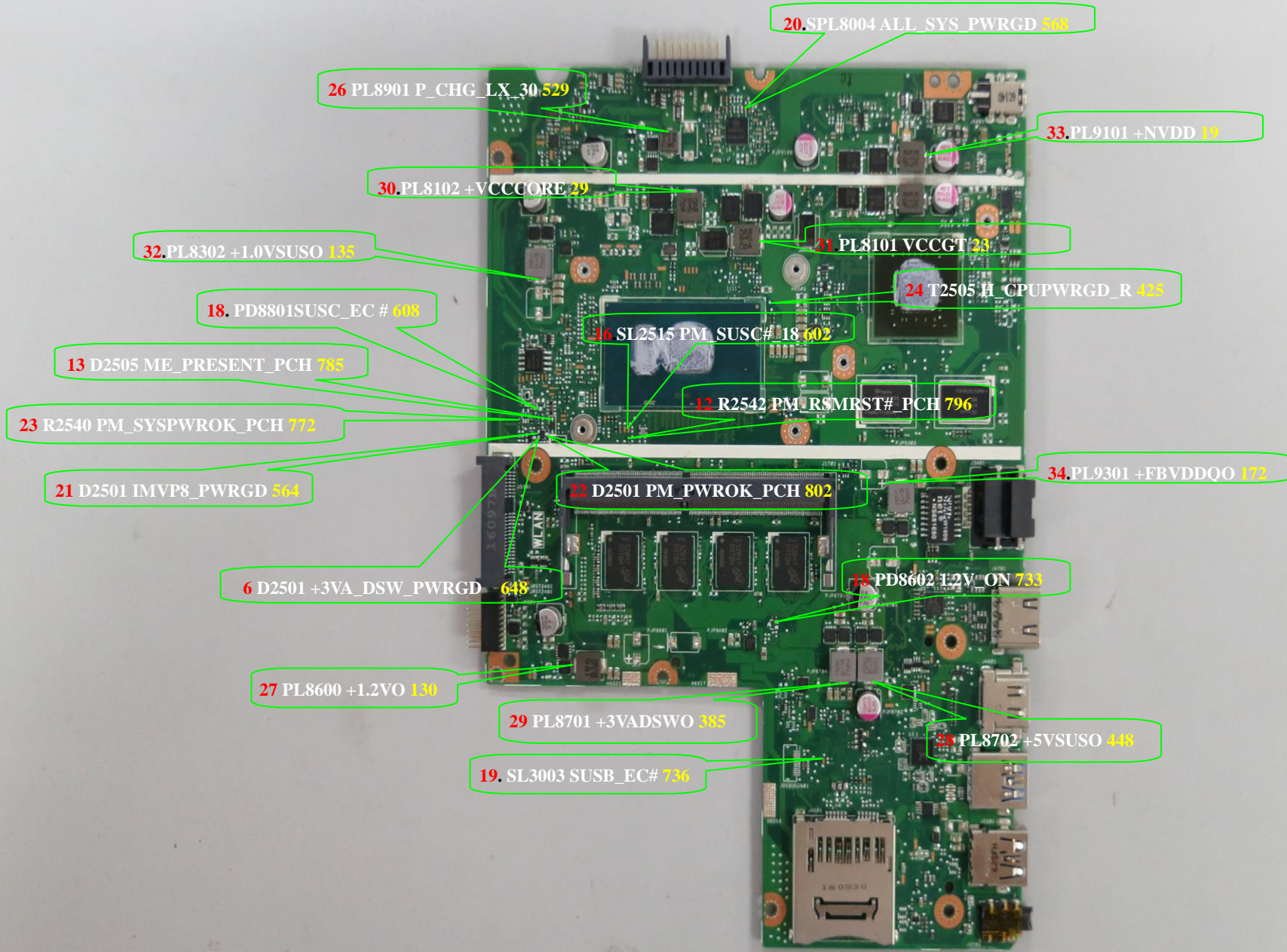
DC-IN Mode

- 1 +3VA/+3VA_EC
- (to EC) 2 EC_RST#
- (EC to power) 3 SVSUS_ON
3VA_DSW_ON
+3VA_DSW/+SVSUS/+12VSUS
- (PCH to EC) 4 ME_SusPwrDnAck_R
- (power to EC) 5 SVSUS_FWRGD
- (EC to PCH) 6 PM_RSMRST#
- (EC to PCH) 7 AC_PRESENT
- (to EC) 8 FWR_SW#
- (EC to PCH) 9 PM_FWRBTN#
- (PCH to EC) 10 PM_SLP_A#
- (PCH to EC) 11 PM_SUSC#
- 12 PM_SUSB#/SLP_LAN#
(PCH to EC) (PCH to power)
- (EC to power) ME_SLP_M_EC#
- (EC to power) 13 SUSC_EC#
- +1.2V/+2.5V/+3V/+12V/+VCCST
- (EC to power) 14 SUSB_EC#
- +1.8V/+3V/+SVS/+12VS
+VCCIO/+VccSIG
- VccST/VccPLL
- +VCCSA
- (power to EC) 15 IMVPS_FWRGD
- 16 ALL_SYSTEM_FWRGD
- (EC to PCH) 17 PM_FWROR_PCH
- (PCH to EC) PCH_SUS_STAT#
- (PCH to EC) 18 FLT_RST#
- +VCCGT
- THERMTRIP#

X541UV/UA Power-On Sequence
Timing Diagram Rev.0.1



Signal Measure Point-Bottom



Signal Measure Point-Top

