

DATA SHEET

TDA4863J; TDA4863AJ Vertical deflection booster

Product specification
File under Integrated Circuits, IC02

2000 Aug 17

Vertical deflection booster

TDA4863J; TDA4863AJ

FEATURES

- Power amplifier with differential inputs
- Output current up to 3 A (p-p)
- High vertical deflection frequency up to 200 Hz
- High linear sawtooth signal amplification
- Flyback generator:
 - TDA4863J: separate adjustable flyback supply voltage up to 60 V
 - TDA4863AJ: internally doubled supply voltage (two supply voltages only for DC-coupled outputs).

GENERAL DESCRIPTION

The TDA4863J and TDA4863AJ are deflection boosters for use in vertical deflection systems for frame frequencies up to 200 Hz.

The TDA4863J needs a separate flyback supply voltage, so the supply voltages are independently adjustable to optimize power consumption and flyback time.

For the TDA4863AJ the flyback supply voltage will be generated internally by doubling the supply voltage and therefore a separate flyback supply voltage is not needed.

Both circuits provide differential input stages and fit well with the TDA485X monitor deflection controller family.

QUICK REFERENCE DATA

Measurements referenced to ground (pin GND).

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{P1}	supply voltage 1 (pin 1)		9	–	30	V
V_{P2}	supply voltage 2 for vertical output (pin 3)		$V_{P1} - 1$	–	60	V
V_{FB}	flyback supply voltage of TDA4863J (pin 2)		$V_{P1} - 1$	–	60	V
V_{P3}	flyback generator output voltage of TDA4863AJ (pin 2)	$I_5 = -1.5$ A	0	–	$V_{P1} + 2.2$	V
$I_{5(p-p)}$	vertical output current (pin 5) (peak-to-peak value)		–	–	3	A
I_{P1}	supply current 1 (pin 1)	during scan	–	6	10	mA
I_{P2}	quiescent supply current 2 (pin 3)	$I_5 = 0$	–	25	60	mA
V_{INP}	input voltage (pin 7)		1.6	–	$V_{P1} - 0.5$	V
V_{INN}	input voltage (pin 6)		1.6	–	$V_{P1} - 0.5$	V
T_{amb}	ambient temperature		–20	–	+75	°C

ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
TDA4863J	DBS7P	plastic DIL-bent-SIL power package; 7 leads (lead length 12/11 mm); exposed die pad	SOT524-1
TDA4863AJ			

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BLOCK DIAGRAM

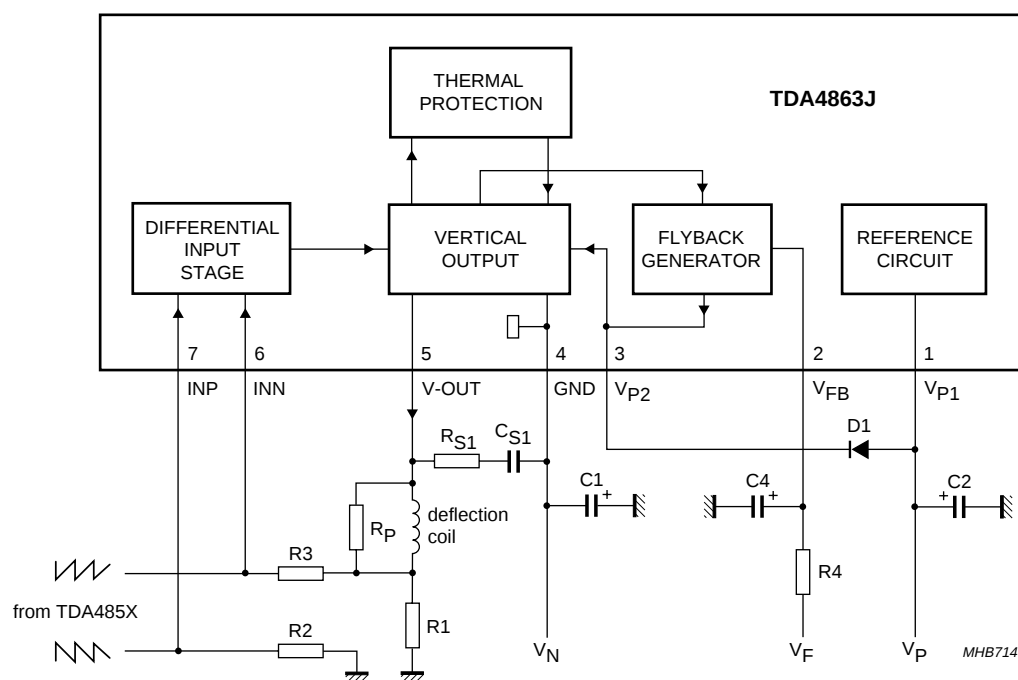


Fig.1 Block diagram of TDA4863J.

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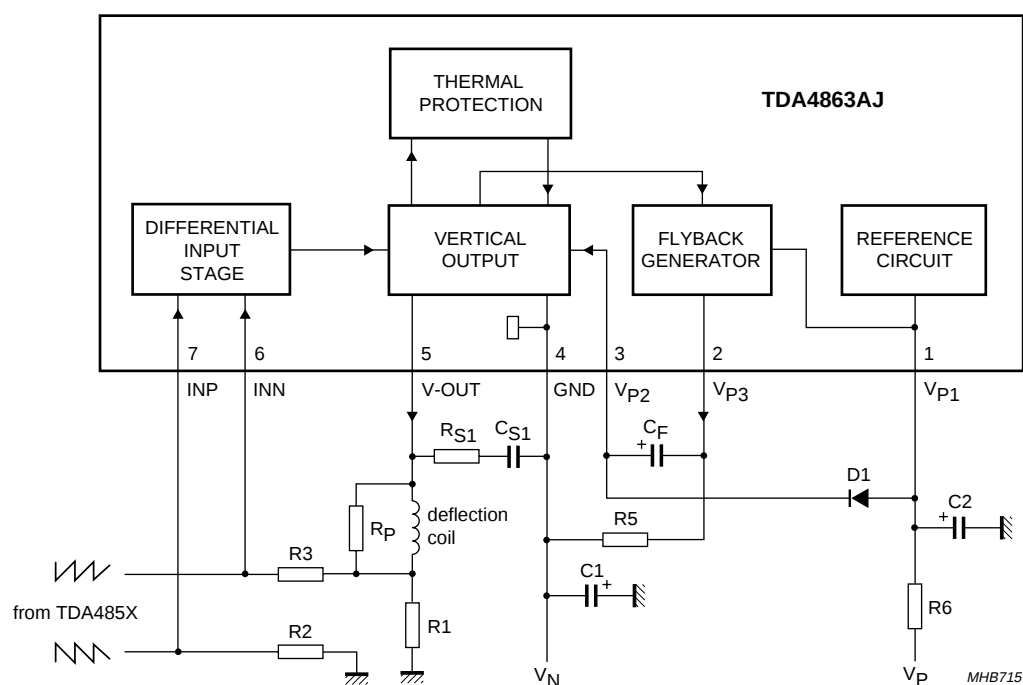


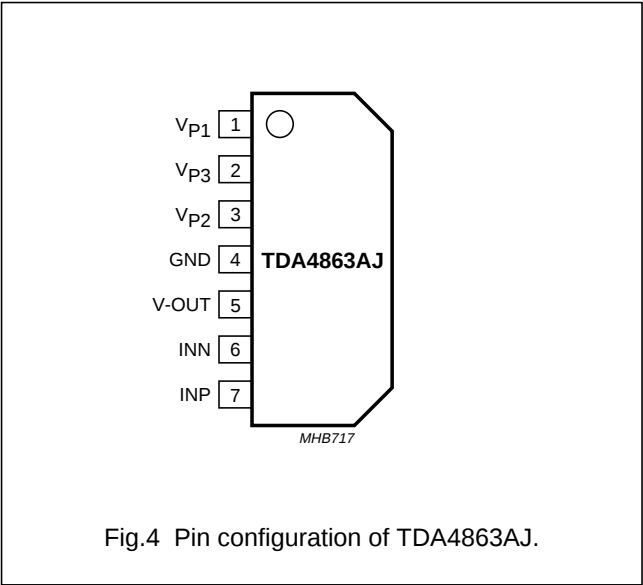
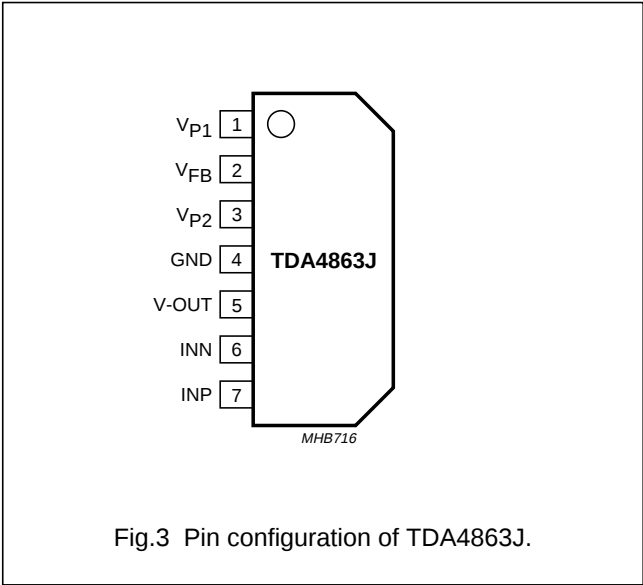
Fig.2 Block diagram of TDA4863AJ.

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PINNING

SYMBOL	PIN		DESCRIPTION
	TDA4863J	TDA4863AJ	
V _{P1}	1	1	positive supply voltage 1
V _{FB}	2	–	flyback supply voltage
V _{P3}	–	2	flyback generator output
V _{P2}	3	3	supply voltage 2 for vertical output
GND	4	4	ground or negative supply voltage
V-OUT	5	5	vertical output
INN	6	6	inverted input of differential input stage
INP	7	7	non-inverted input of differential input stage



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FUNCTIONAL DESCRIPTION

Both the TDA4863J and TDA4863AJ consist of a differential input stage, a vertical output stage, a flyback generator, a reference circuit and a thermal protection circuit.

The TDA4863J operates with a separate flyback supply voltage (see Fig.1) while the TDA4863AJ generates the flyback voltage internally by doubling the supply voltage (see Fig.2).

Differential input stage

The differential sawtooth input signal (e.g. coming from the deflection controller family TDA485X) is connected to the inputs (inverted signal to pin 6 and non-inverted signal to pin 7). The vertical feedback signal is superimposed on the inverted signal on pin 6.

Vertical output and thermal protection

The vertical output stage is a quasi-complementary class-B amplifier with a high linearity.

The output stage is protected against thermal overshoots. For a junction temperature $T_j > 150\text{ }^{\circ}\text{C}$ this protection will be activated and will reduce then the deflection current (I_5).

Flyback generator

The flyback generator supplies the vertical output stage during flyback.

The TDA4863J is used with separate flyback supply voltage to achieve a short flyback time with minimized power dissipation.

The TDA4863AJ needs a capacitor C_F between pins 2 and 3 (see Fig.2). Capacitor C_F is charged during scan, using the external diode D1 and resistor R5. During flyback the cathode of capacitor C_F is connected to the positive supply voltage and the flyback voltage is then twice the supply voltage. For the TDA4863AJ the resistor R6 in the positive supply line can be used to reduce the power consumption.

In parallel with the deflection coil a damping resistor R_P and an RC combination ($R_{S1} = 5.6\text{ }\Omega$ and $C_{S1} = 100\text{ nF}$) are needed. Furthermore, another additional RC combination ($R_{S2} = 5.6\text{ }\Omega$ and $C_{S2} = 47\text{ to }150\text{ nF}$) can be used to minimize the noise effect and the flyback time (see Figs 9 and 10).

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134); voltages referenced to ground (pin 4); unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{P1}	supply voltage 1 (pin 1)		–	40	V
V_{P2}	supply voltage 2 (pin 3)		–	60	V
V_{FB}	flyback supply voltage of TDA4863J (pin 2)		–	60	V
V_{P3}	flyback generator output voltage of TDA4863AJ (pin 2)		0	$V_{P1} + 3$	V
$V_{i(6,7)}$	input voltage on pins 6 and 7		–	V_{P1}	V
$V_{o(5)}$	output voltage on pin 5		–	62	V
I_{P2}	supply current 2 (pin 3)		–	± 1.8	A
$I_{o(5)}$	output current (pin 5)	note 1	–	± 1.8	A
I_2	flyback current on pin 2		–	± 1.8	A
T_{stg}	storage temperature		–25	+150	°C
T_{amb}	ambient temperature		–20	+75	°C
T_j	junction temperature	note 1	–	150	°C
V_{es}	electrostatic discharge voltage on all pins	note 2	–300	+300	V

Notes

- Internally limited by thermal protection; will be activated for $T_j \geq 150$ °C.
- Equivalent to discharging a 200 pF capacitor through a 0 Ω series resistor.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	note 1	6	K/W

Note

- To minimize the thermal resistance from mounting base to heatsink [$R_{th(mb-h)}$] follow the recommended mounting instruction: screw mounting preferred; torque = 40 Ncm; use heatsink compound; isolation plate increases $R_{th(mb-h)}$.

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CHARACTERISTICS

$V_{P1} = 25\text{ V}$; $V_4 = 0\text{ V}$; $T_{\text{amb}} = 25\text{ °C}$; voltages referenced to ground (pin 4); unless otherwise specified.

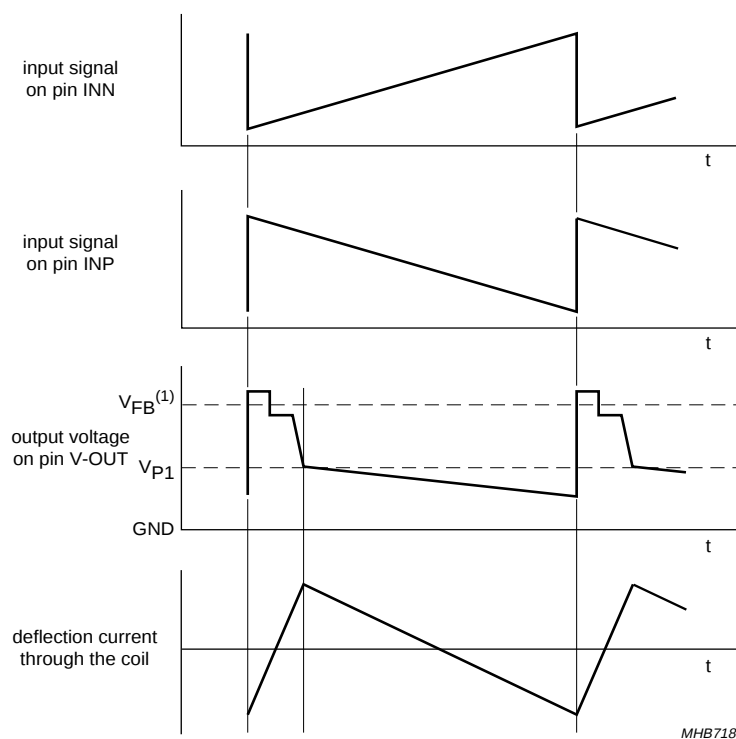
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supplies						
V _{P1}	supply voltage 1 (pin 1)		9	–	30	V
V _{P2}	supply voltage 2 (pin 3)		V _{P1} – 1	–	60	V
V _{FB}	flyback supply voltage of TDA4863J (pin 2)		V _{P1} – 1	–	60	V
V _{P3}	flyback generator output voltage of TDA4863AJ (pin 2)	I ₅ = –1.5 A	0	–	V _{P1} + 2.2	V
I _{P1}	supply current 1 (pin 1)	during scan	–	6	10	mA
I _{P2}	quiescent supply current 2 (pin 3)	I ₅ = 0	–	25	60	mA
Differential input stage						
V _{INP}	input voltage (pin 7)		1.6	–	V _{P1} – 0.5	V
V _{INN}	input voltage (pin 6)		1.6	–	V _{P1} – 0.5	V
I _{INP}	input quiescent current (pin 7)		–	–100	–500	nA
I _{INN}	input quiescent current (pin 6)		–	–100	–500	nA
Flyback generator						
I ₂	current during flyback		–	–	±1.5	A
V ₃₋₂	voltage drop during flyback of TDA4863J reverse	I ₅ = –1 A	–	–1.5	–	V
		I ₅ = –1.5 A	–	–2	–	V
	forward	I ₅ = 1 A	–	2.2	–	V
		I ₅ = 1.5 A	–	2.5	–	V
V ₂₋₁	voltage drop during flyback of TDA4863AJ reverse	I ₅ = –1 A	–	–1.5	–	V
		I ₅ = –1.5 A	–	–2	–	V
	forward	I ₅ = 1 A	–	2.2	–	V
		I ₅ = 1.5 A	–	2.5	–	V
Vertical output stage; see Fig.5						
I ₅	vertical deflection output current		–	–	±1.5	A
I _{5(p-p)}	vertical deflection output current on pin 5 (peak-to-peak value)		–	–	3	A
V ₅₋₄	output saturation voltage to ground	I ₅ = 1 A	–	1.4	1.7	V
		I ₅ = 1.5 A	–	1.8	2.3	V
V ₅₋₃	output saturation voltage to V _{P2}	I ₅ = 1 A	–2.3	–2	–	V
		I ₅ = 1.5 A	–2.8	–2.3	–	V
LIN	non-linearity of output signal	note 1	–	–	1	%

Note

1. Deviation of the output slope at a constant input slope.

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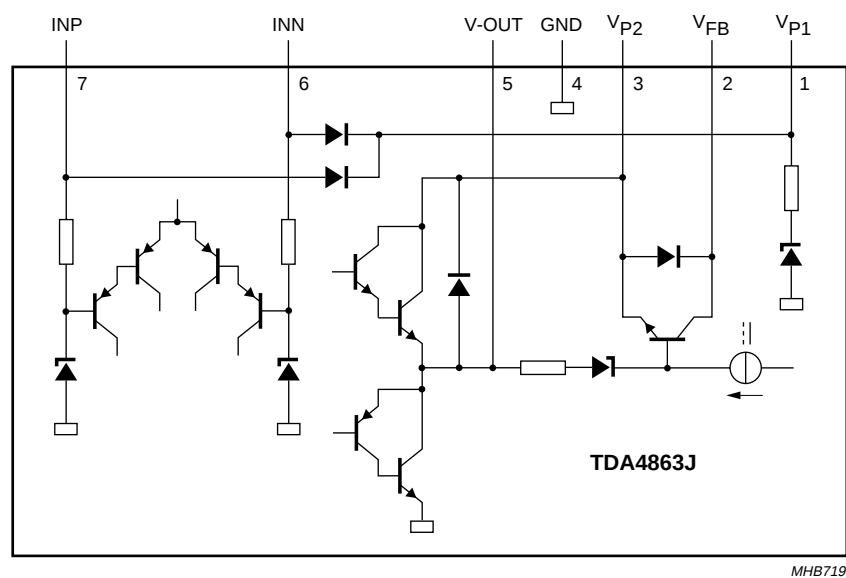
(1) V_{FB} for TDA4863J; $2V_{P1}$ for TDA4863AJ.

Fig.5 Timing diagram.

Vertical deflection booster

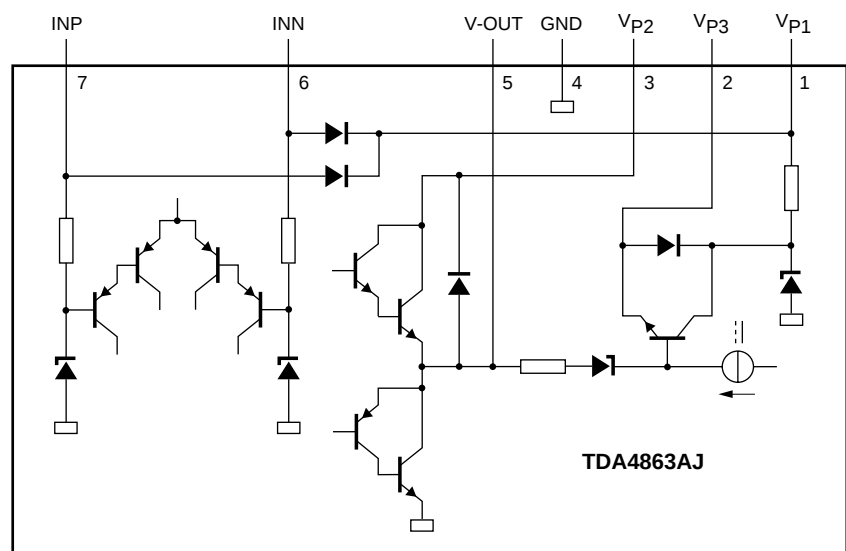
TDA4863J; TDA4863AJ

INTERNAL PIN CONFIGURATION



MHB719

Fig.6 Internal circuits of TDA4863J.



MHB720

Fig.7 Internal circuits of TDA4863AJ.

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APPLICATION INFORMATION

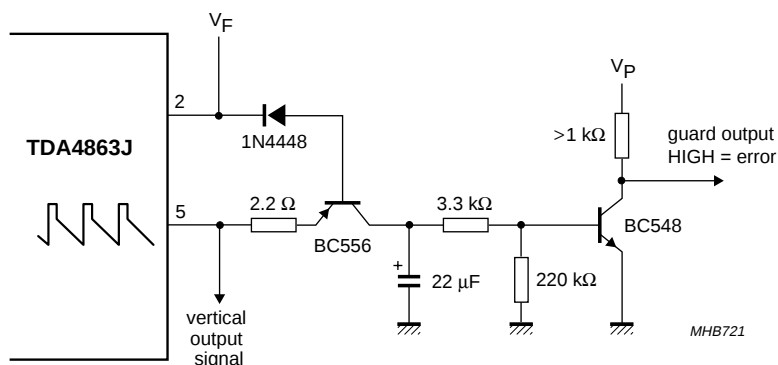
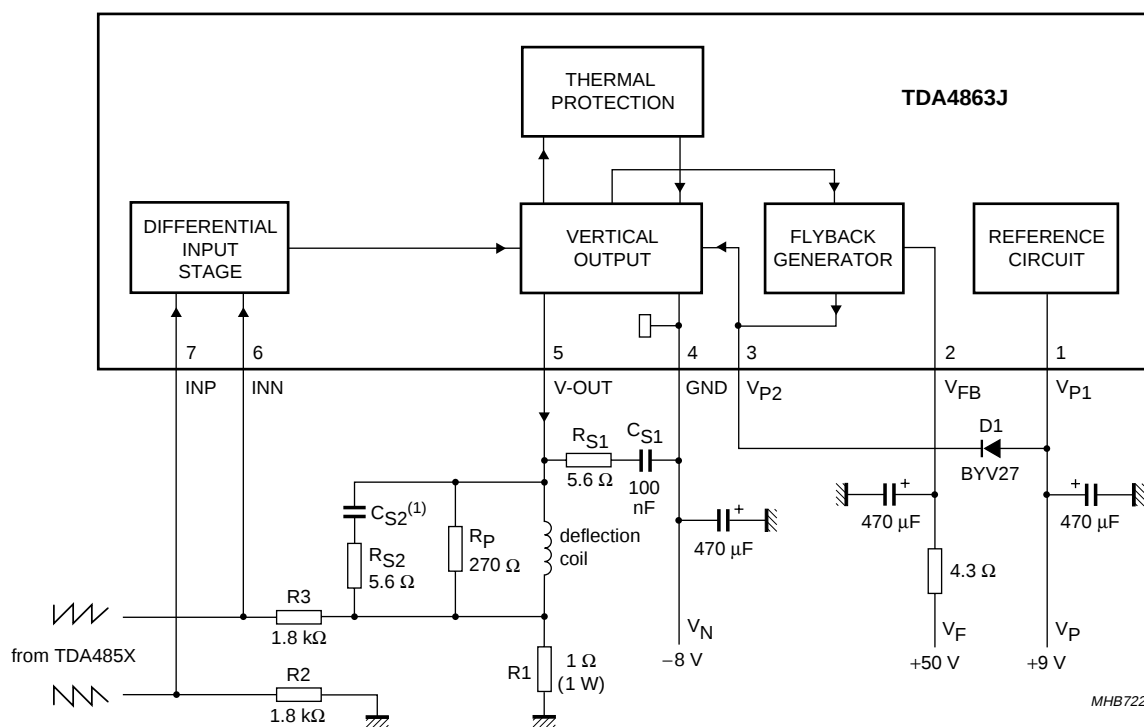


Fig.8 Application circuit with TDA4863J for external guard signal generation.



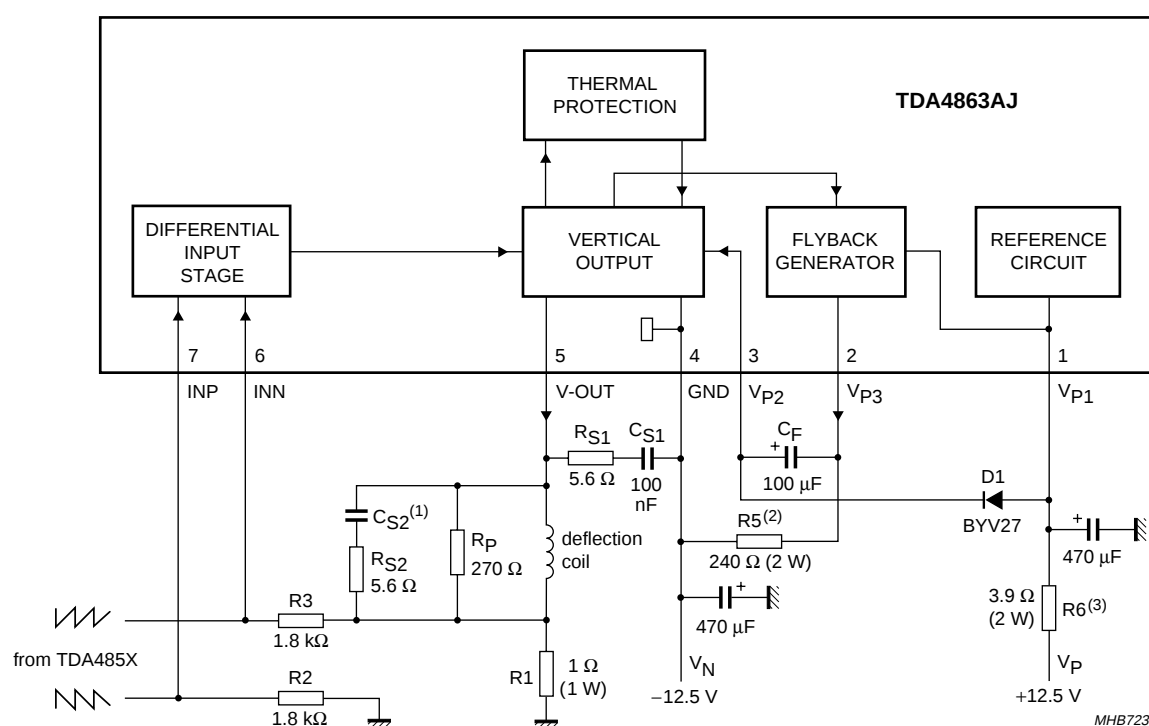
Attention: the heatsink of the IC must be isolated against ground of the application (it is connected to pin 4).

(1) With C_{S2} (typical value between 47 and 150 nF) the flyback time and the noise behaviour can be optimized.

Fig.9 Application circuit with TDA4863J.

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Attention: the heatsink of the IC must be isolated against ground of the application (it is connected to pin 4).

- (1) With C_{S2} (typical value between 47 and 150 nF) the flyback time and the noise behaviour can be optimized.
- (2) With R5 capacitor C_F will be charged during scan and the value (typical value between 150 and 270 Ω) depends on I_{defl} , t_{fb} and C_F .
- (3) R6 reduces the power dissipation of the IC. The maximum possible value depends on the application.

Fig.10 Application circuit with TDA4863AJ.

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Example for both TDA4863J and TDA4863AJ

Table 1 Values given from application

SYMBOL	VALUE	UNIT
$I_{\text{defl(max)}}$	0.71	A
L_{deflcoil}	6	mH
R_{deflcoil}	6	Ω
R_P	270	Ω
R_1	1	Ω
R_2	1.8	k Ω
R_3	1.8	k Ω
$V_{\text{FB}}^{(1)}$	50	V
T_{amb}	60	$^{\circ}\text{C}$
T_{deflcoil}	75	$^{\circ}\text{C}$
$R_{\text{th(j-mb)}}$	6	K/W
$R_{\text{th(mb-amb)}}$	8	K/W

Note

- For TDA4863J only.

Table 2 Calculated values

SYMBOL	VALUE		UNIT
	TDA4863J	TDA4863AJ	
V_{P1}	9	12.5	V
V_N	-8	-12.5	V
P_{tot}	3.2	4.4	W
P_{defl}	1.2	1.2	W
P_{IC}	2	3.2	W
$R_{\text{th(tot)}}$	14	14	K/W
$T_{\text{j(max)}}$	88	105	$^{\circ}\text{C}$

V_{P1} , V_N and V_{FB} are referenced to ground of application; voltages are calculated with +10% tolerances.

Calculation formulae for supply voltages:

$$V_{P1} = -V_{5-3} + (R_1 + R_{\text{deflcoil}}) \times I_{\text{defl(max)}} - U'_L + U_{D1}$$

$$V_N = V_{5-4} + (R_1 + R_{\text{deflcoil}}) \times I_{\text{defl(max)}} + U'_L$$

where

$$U'_L = L_{\text{deflcoil}} \times 2I_{\text{defl(max)}} \times f_v$$

f_v = vertical deflection frequency

U_{D1} = forward voltage drop across D1.

Calculation formulae for power consumption:

$$P_{\text{IC}} = P_{\text{tot}} - P_{\text{defl}}$$

$$P_{\text{tot}} = (V_{P1} - U_{D1}) \times \frac{I_{\text{defl(max)}}}{4} + V_N \times \frac{I_{\text{defl(max)}}}{4} + (V_{P1} - V_N) \times 0.01 \text{ A} + 0.2 \text{ W}$$

$$P_{\text{defl}} = \frac{R_{\text{deflcoil}} + R_1}{3} \times I_{\text{defl(max)}}^2$$

where

P_{IC} = power dissipation of the IC

P_{tot} = total power dissipation

P_{defl} = power dissipation of the deflection coil.

Calculation formulae for maximum required thermal resistance for the heatsink at $T_{\text{j(max)}} = 110^{\circ}\text{C}$:

$$R_{\text{th(mb-amb)}} = \left(\frac{T_{\text{j(max)}} - T_{\text{amb}}}{P_{\text{IC}}} \right) - R_{\text{th(j-mb)}} = 19 \text{ K/W (max.)}$$

Table 3 t_{flb} as a function of V_{FB} for TDA4863J

t_{flb} (μs)	V_{FB} (V)
350	30
250	40
210	50

Table 4 t_{flb} as a function of V_{P1} and V_N for TDA4863AJ

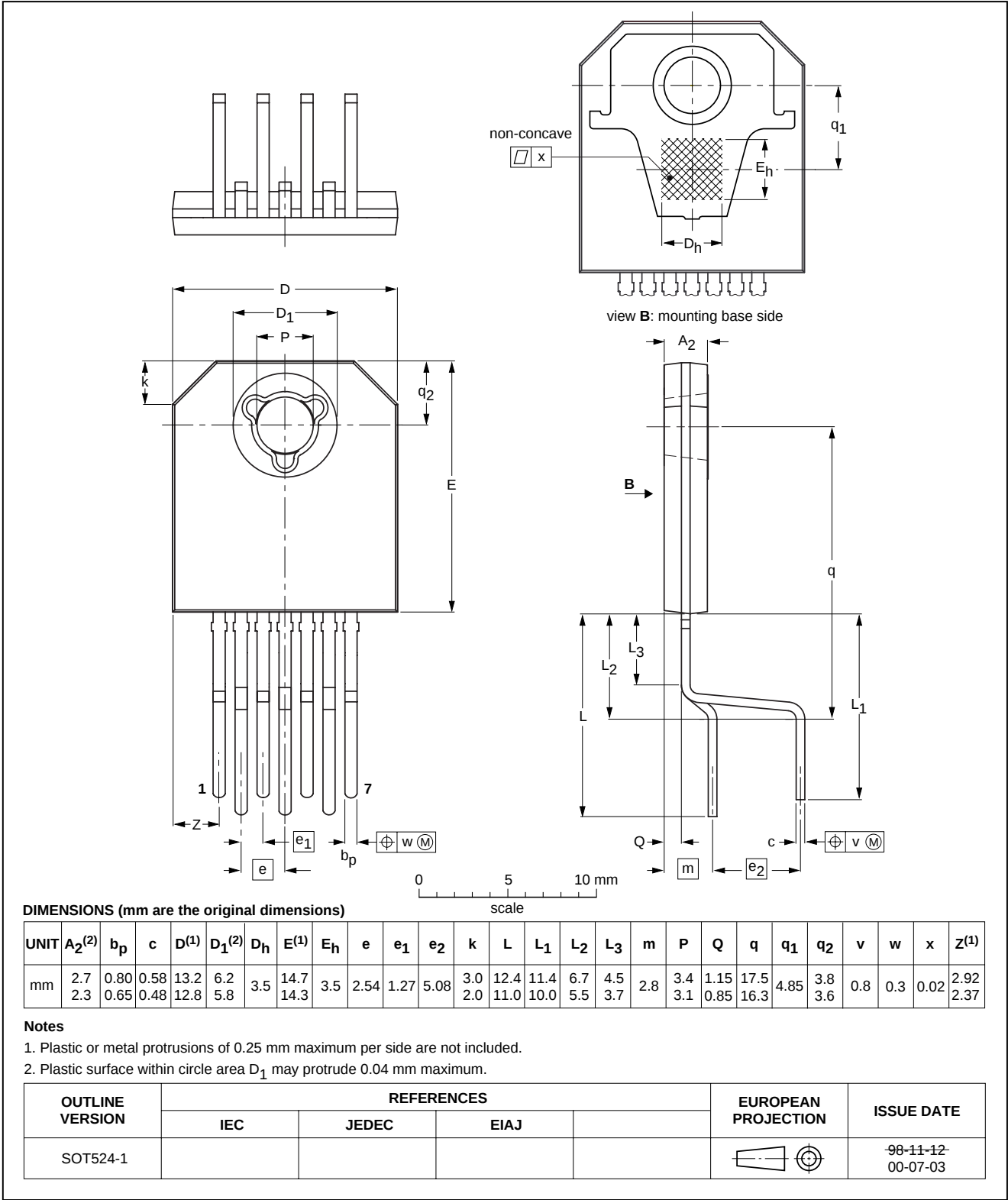
t_{flb} (μs)	V_{P1} (V)	V_N (V)	P_{IC} (W)	R_6 (Ω)
360	10	-10	2.5	1
290	12.5	-12.5	3.2	3.9
240	15	-15	3.9	6.8

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PACKAGE OUTLINE

DBS7P: plastic DIL-bent-SIL power package; 7 leads (lead length 12/11 mm); exposed die pad SOT524-1



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SOLDERING

Introduction to soldering through-hole mount packages

This text gives a brief insight to wave, dip and manual soldering. A more in-depth account of soldering ICs can be found in our *“Data Handbook IC26; Integrated Circuit Packages”* (document order number 9398 652 90011).

Wave soldering is the preferred method for mounting of through-hole mount IC packages on a printed-circuit board.

Soldering by dipping or by solder wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joints for more than 5 seconds.

The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature ($T_{stg(max)}$). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

Manual soldering

Apply the soldering iron (24 V or less) to the lead(s) of the package, either below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

Suitability of through-hole mount IC packages for dipping and wave soldering methods

PACKAGE	SOLDERING METHOD	
	DIPPING	WAVE
DBS, DIP, HDIP, SDIP, SIL	suitable	suitable ⁽¹⁾

Note

1. For SDIP packages, the longitudinal axis must be parallel to the transport direction of the printed-circuit board.

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DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS ⁽¹⁾
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Note

1. Please consult the most recently issued data sheet before initiating or completing a design.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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NOTES

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