Philips Semiconductors

TV signal processor-Teletext decoder with
embedded µ-Controller

TDA955X/6X/8X PS/N1 series

Tentative Device Specification

2000 Jun 22

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Table 1 Pin functions for various versions

IC version	FM-PLL version				QSS VERSION							
					AM/FM MODE [1]		NORMAL MODE					
AMFM bit	0				1		0					
East-West Y/N	N		Y		N		N			Y		
CMB1/CMB0 bits	00	01/10/11	00	01/10/11	–		00	01/10/11		00	01/10/11	
AM bit	–	–	–	–	0	1	–	0	1	–	0	1
Pin 20	AVL		EWD		DECSDEM		AVL			EWD		
Pin 28	AUDEEM				SIFIN1		SIFIN1					
Pin 29	DECSDEM				SIFIN2		SIFIN2					
Pin 31	SNDPLL				SNDPLL	SIFAGC [2]	SIFAGC					
Pin 32	SNDIF [3]	REFO	AVL/SNDIF [3]	REFO	AUDEEM/AMOUT		AMOUT	REFO		AMOUT	REFO	
Pin 35	AUDEXT				AUDEXT		AUDEXT	QSSO	AMOUT	AUDEXT	QSSO	AMOUT
Pin 44	AUDOUT				controlled audio out		controlled AM out					

Note

1. When the AM/FM mode is chosen several pins have a different function. For this reason this mode can be selected only when the adapted application is used.

2. When in the AM/FM mode the AM bit is activated a capacitor with a value of about 2.2 µF has to be switched externally in parallel with the sound PLL filter

3. When additional (external) selectivity is required for FM-PLL system pin 32 can be used as sound IF input. This function is selected by means of SIF bit in subaddress 21H.