



Table 3.1 AU6987-GHL Pin Descriptions

Pin #	Pin Name	I/O	Description
1	GND	GND	Ground
2	VDD	I	1.8V power
3	DM	I/O	USB DM
4	DP	I/O	USB DP
5	REXT	I	External resistor 330 to ground
6	VD33	I	3.3V power
7	VS33P	GND	Ground
8	VSSA	GND	Ground
9	XI	I	12 MHz oscillator input
10	XO	O	12 MHz oscillator output
11	VDDA	I	1.8V power for PLL
12	AGND5V	I	Ground
13	AVDD5V	I	5V input power pin
14	VDD3V	O	3.3V output power pin
15	V18	O	1.8V output for Core
16	WPN	O	Flash Write Protect Pin(Low Active)
17	VDDH	I	3.3V input power pin
18	VSSH	GND	Ground
19	GPO	O	blanking when system access
20	FMWP	I	Flash Write Protect Pin by Firmware(High Active)
21	FMENABN3	O	Flash 3 select pin
22	FMENABN2	O	Flash 2 select pin
23	FMENABN1	O	Flash 1 select pin
24	FMENABN0	O	Flash 0 select pin
25	FMRBN1	I	Flash 1 ready pin
26	FMRBN2	I	Flash 2 ready pin
27	FMDATH7	I/O	Flash high data 7 pin
28	FMDATH6	I/O	Flash high data 6 pin
29	FMDATH5	I/O	Flash high data 5 pin
30	FMDATH4	I/O	Flash high data 4 pin
31	FMDATH3	I/O	Flash high data 3 pin



Pin #	Pin Name	I/O	Description
32	FMDATH2	I/O	Flash high data 2 pin
33	FMDATH1	I/O	Flash high data 1 pin
34	FMDATH0	I/O	Flash high data 0 pin
35	FMRDN	O	Flash read signal
36	FMWRN	O	Flash write signal
37	FMDATL7	I/O	Flash low data 7 pin
38	FMDATL6	I/O	Flash low data 6 pin
39	FMDATL5	I/O	Flash low data 5 pin
40	FMDATL4	I/O	Flash low data 4 pin
41	FMDATL3	I/O	Flash low data 3 pin
42	FMDATL2	I/O	Flash low data 2 pin
43	FMDATL1	I/O	Flash low data 1 pin
44	FMDATL0	I/O	Flash low data 0 pin
45	FMCLE	O	Flash command latch pin
46	FMALE	O	Flash address latch pin
47	FMRBN3	I	Flash 3 ready pin
48	FMRBN0	I	Flash 0 ready pin