

Detailed Circuit Description (Continued)
Phase Comparator State Diagrams

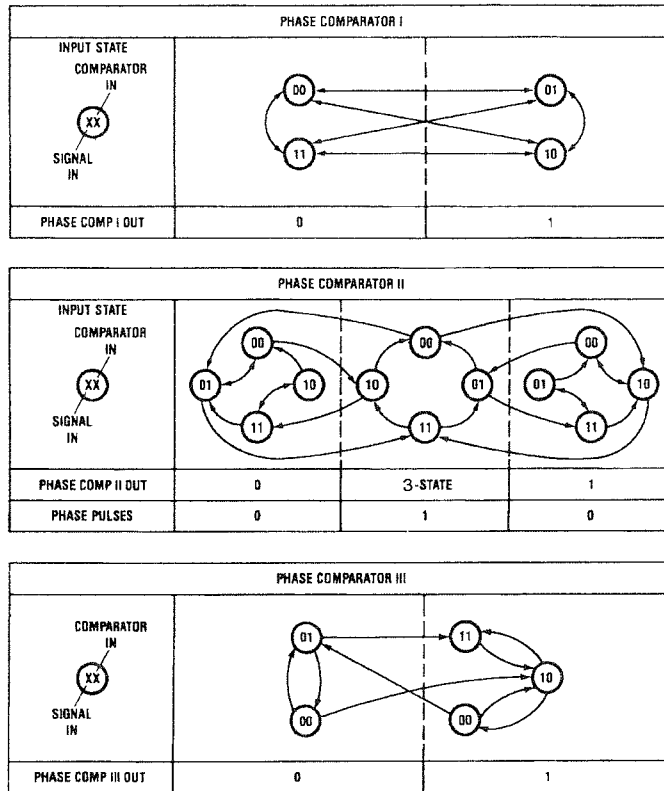


FIGURE 5. PLL State Tables

