

## 5. Input Terminal Pin Assignment

### 5.1. Input Signal & Power

Connector : FI-RE51S-HF (JAE/UJU)

PIN No.	Description		PIN No.	Description	
1	VDD(12V)		26	Even LVDS Signal	
2	VDD(12V)		27		
3	VDD(12V)		28		
4	VDD(12V)		29		
5	VDD(12V)		30		
6	No Connection or GND		31		
7	GND		32		
8	GND		33		
9	GND		34		
10	Odd LVDS Signal	RO[0]N	35		
11		RO[0]P	36		
12		RO[1]N	37		
13		RO[1]P	38		
14		RO[2]N	39		GND
15		RO[2]P	40		No Connection
16		GND	41		No Connection
17		RO[CLK]N	42	No Connection	
18		RO[CLK]P	43	No Connection	
19		GND	44	No Connection	
20		RO[3]N	45	LVDS Option	
21		RO[3]P	46	No Connection	
22		RO[4]N	47	No Connection	
23		RO[4]P	48	No Connection	
24	GND		49	No Connection	
25	Even LVDS	RE[0]N	50	No Connection	
			51	No Connection	

Note)(1) No Connection: This PINS are only used for SAMSUNG internal using.

(2) LVDS OPTION : If this PIN : HIGH (3.3 V) or OPEN → Normal LVDS format  
: LOW (GND) → JEIDA LVDS format

SEQUENCE : On =  $V_{DD}(T1) \geq \text{LVDS Option} \geq \text{Interface Signal}(T2)$

OFF =  $\text{Interface Signal}(T3) \geq \text{LVDS Option} \geq V_{DD}$

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